Virtual Workshop Series on Semiconductor R&D: Workshop 4 – Advanced Packaging for Energy Efficient Microelectronics

Hosted by the DOE/EERE Advanced Manufacturing Office (AMO) with the National Institute of Standards and Technology (NIST)
January 12-13, 2022

DRAFT AGENDA

DAY 1: January 12, 12:30 PM – 3:00 PM EDT		
Time	Activity	
12:30 – 1:50	Opening Plenary	
12:30 – 12:45	Welcome from DOE AMO Organizer (Tina Kaarsberg, DOE AMO, Workshop Chair)	
12:45 – 12:55	Opening Remarks (Becca Jones-Albertus, Acting Director, DOE AMO)	
12:55 – 1:25	Keynote Talk (Bahgat Sammakia, Distinguished Professor, Binghamton University)	
1:25 – 1:40	BREAK	
1:40 – 2:40	Panel on Thermal Management for 3D Microelectronics	
1:40 – 2:10	- Muhannad Bakir, Georgia Institute of Technology - Mike Kelly, Amkor - Ravi Mahajan, Intel - Ganesh Subbarayan, Purdue University	
2:10 – 2:40	Panel Q&A on Thermal Management for 3D Microelectronics	
2:40 – 3:00	Day 1 Concluding Remarks (Tina Kaarsberg, DOE AMO, Workshop Chair)	



DAY 2: January 13, 12:30 PM – 3:15 PM EDT	
Time	Activity
12:30 – 12:55	Opening Plenary
12:30 – 12:45	Welcome Back (Tina Kaarsberg, DOE AMO, Workshop Chair)
12:45 – 12:55	Opening Remarks (NIST)
12:55 – 1:55	Panel on Metrology for 3D Microelectronics
12:55 – 1:25	- Shankar Devasenathipathy, Intel - Will Harris, Zeiss - Ken Joyce, Brewer Science - To be announced soon
1:25 – 1:55	Panel Q&A on Metrology for 3D Microelectronics
1:55 – 2:05	BREAK
2:05 – 3:05	Panel on I/O and Interconnects for 3D Microelectronics
2:05 – 2:35	- Henning Braunisch, Intel - Suresh Ramalingam, Xilinx - Raja Swaminathan, AMD - Jie Xue, Cisco Systems
2:35 – 3:05	Panel Q&A on I/O and Interconnects for 3D Microelectronics
3:05 – 3:15	Webinar Concluding Remarks (Tina Kaarsberg, DOE AMO, Workshop Chair)